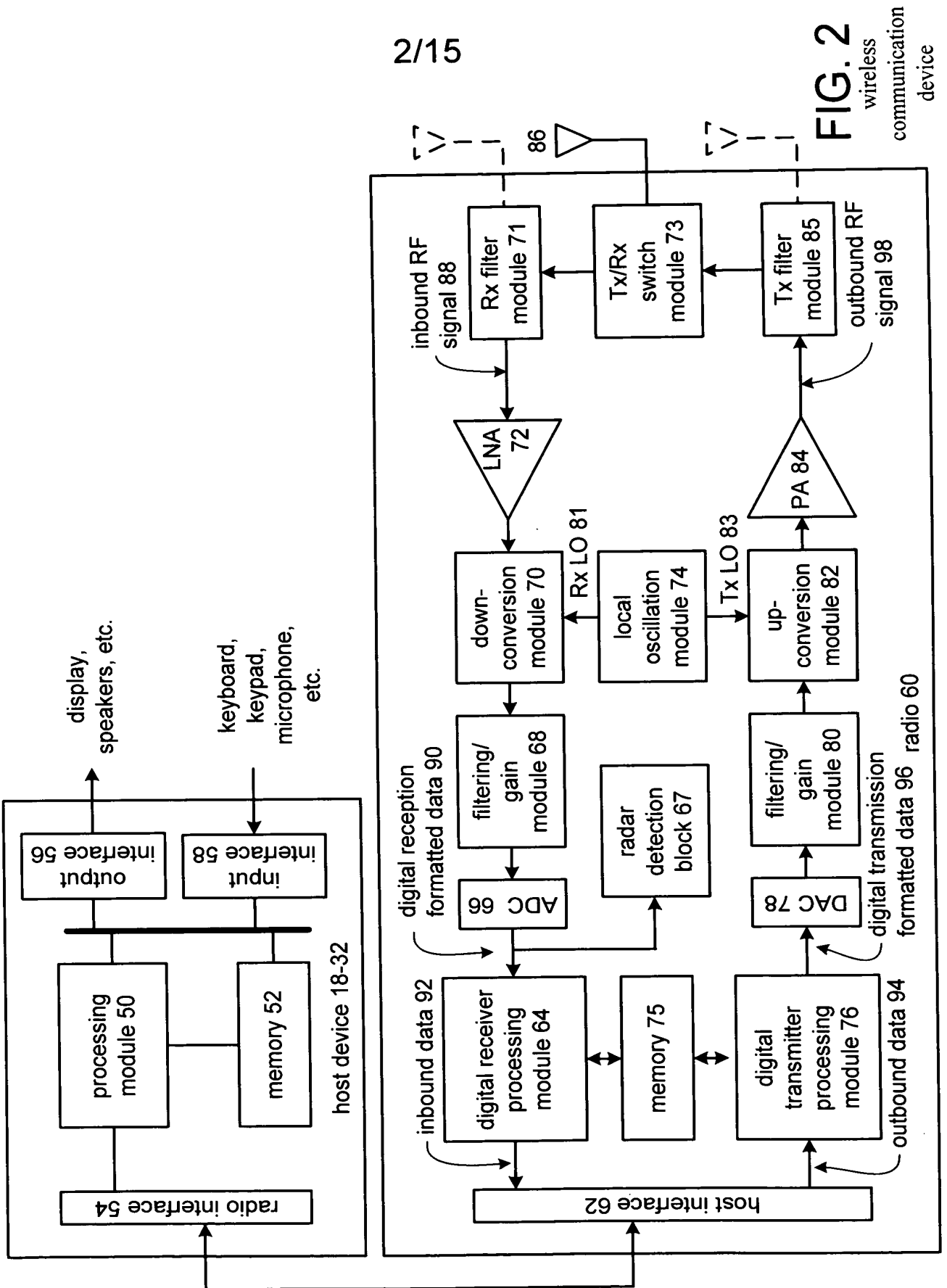


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FIG. 1 communication system

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**FIG. 2**  
wireless  
communication  
device

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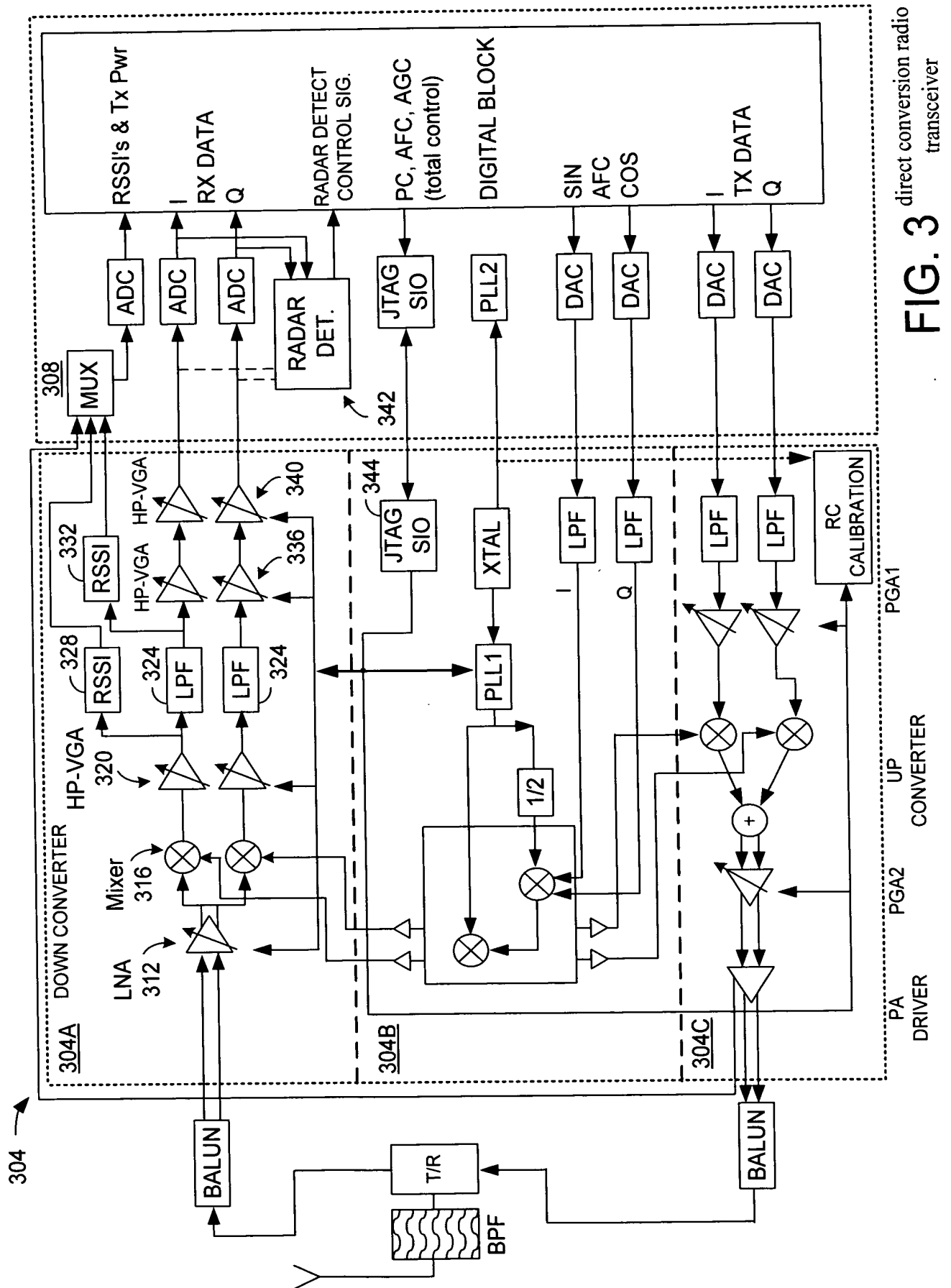


FIG. 3 direct conversion radio transceiver

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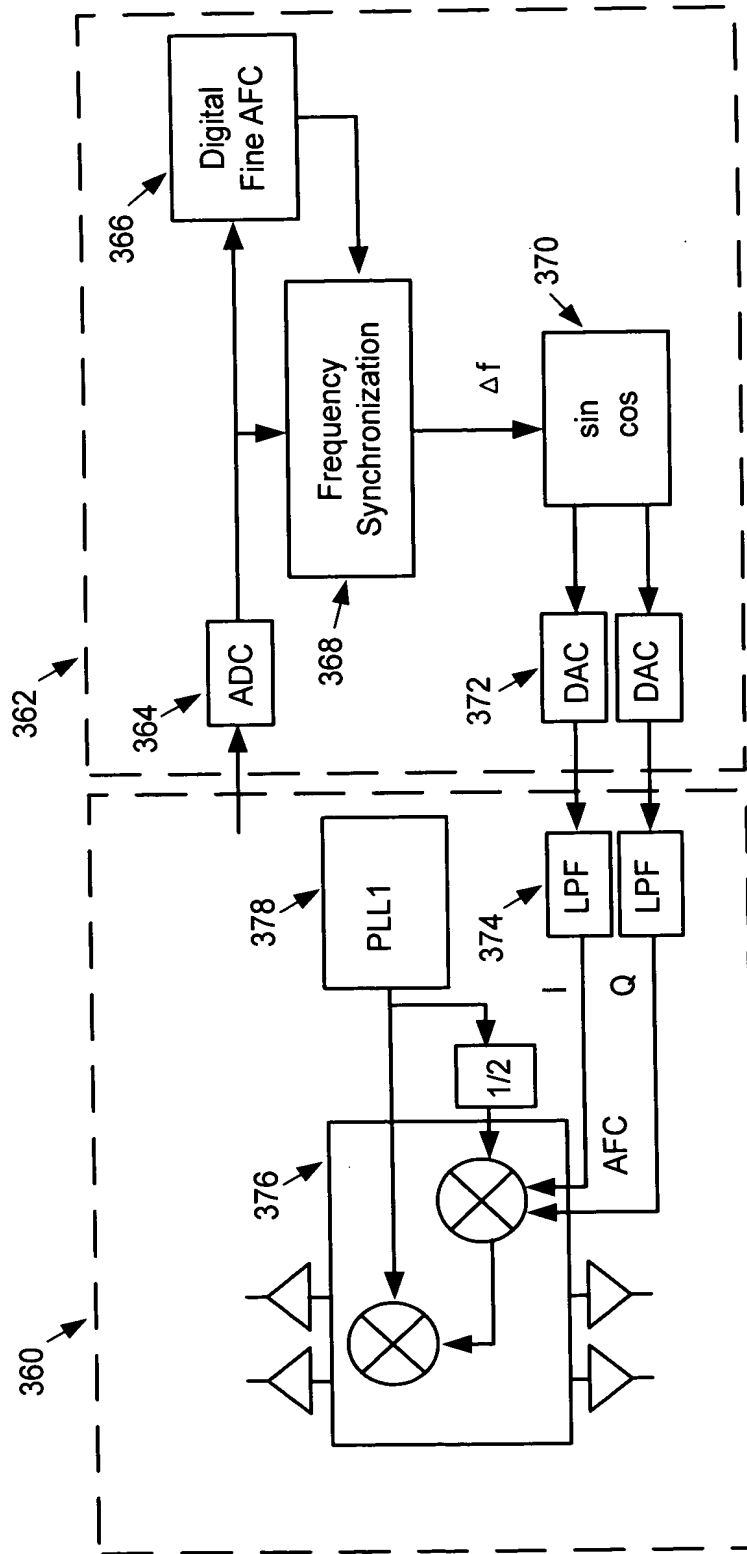


FIG. 4 receiver automatic frequency control circuit

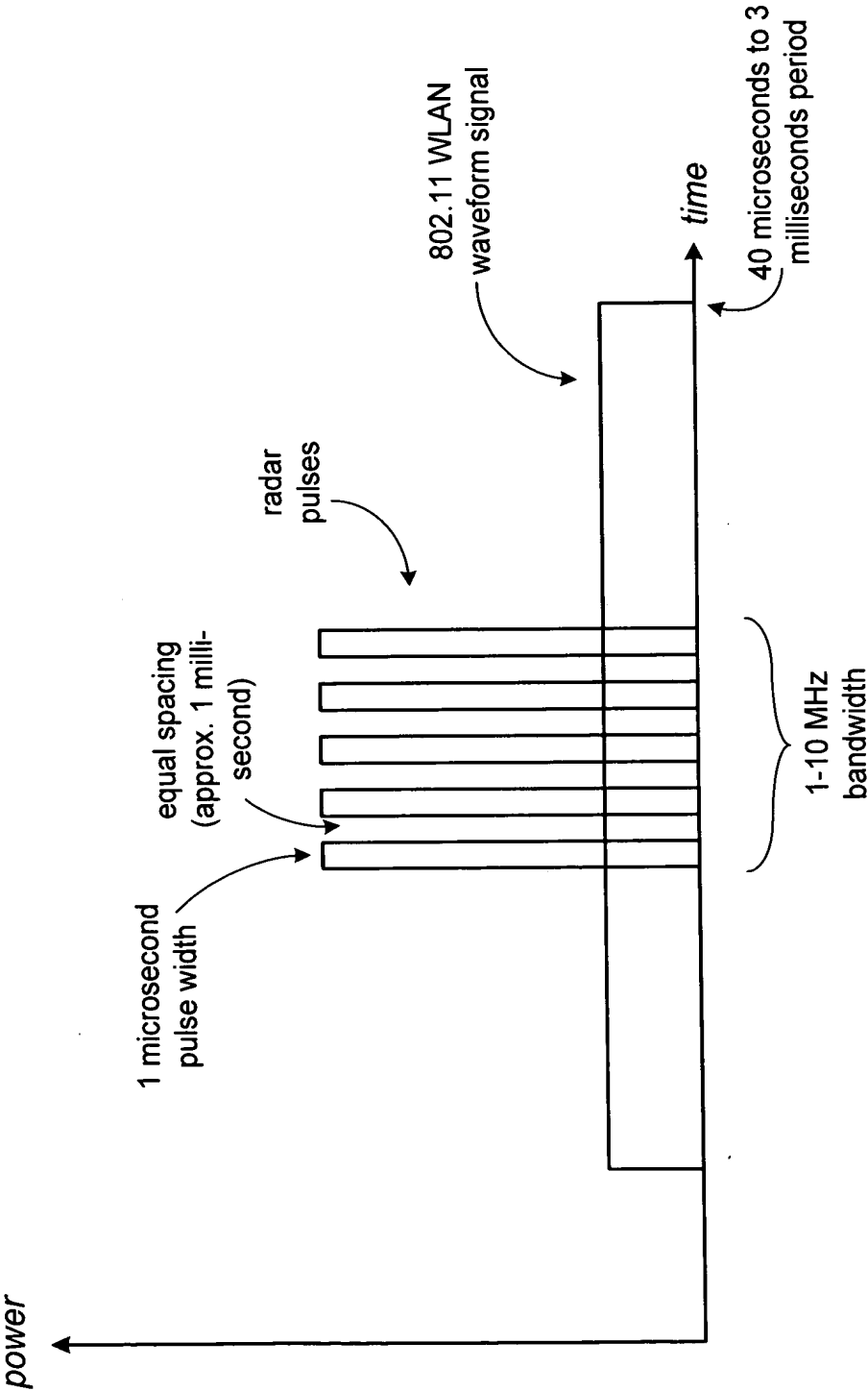
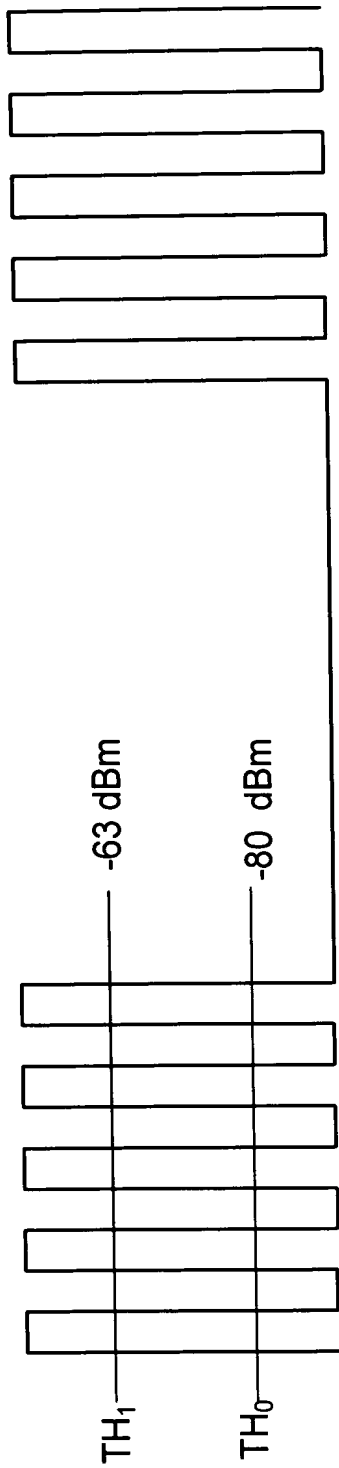
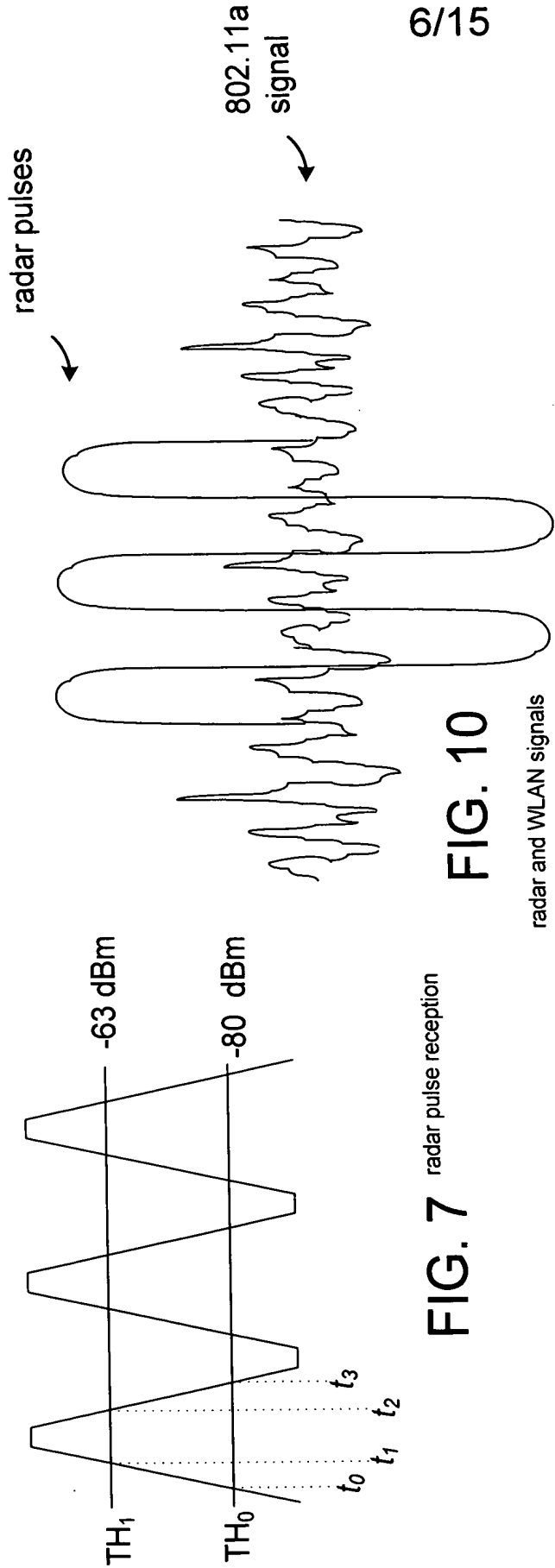
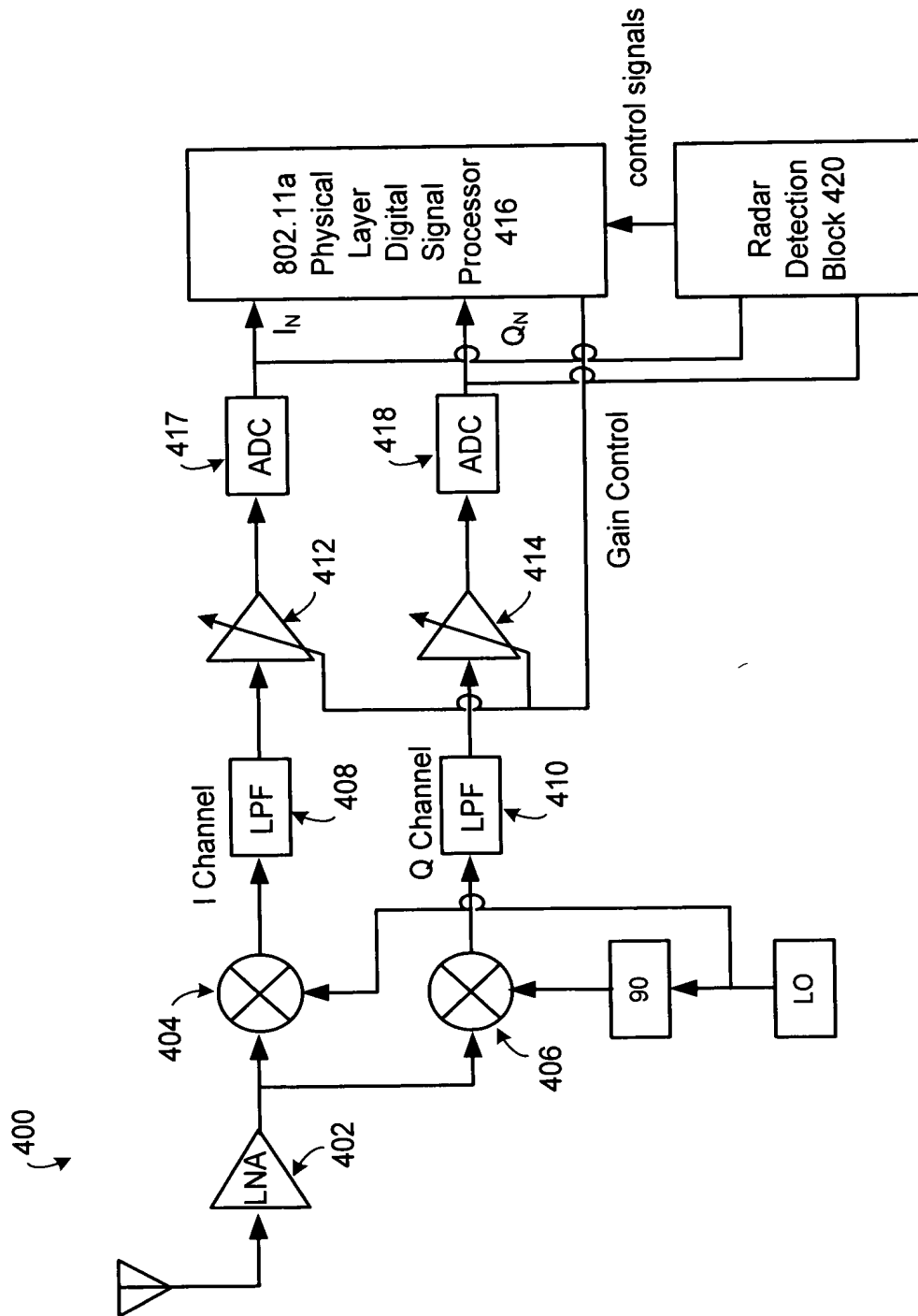


FIG. 5 radar and WLAN signals



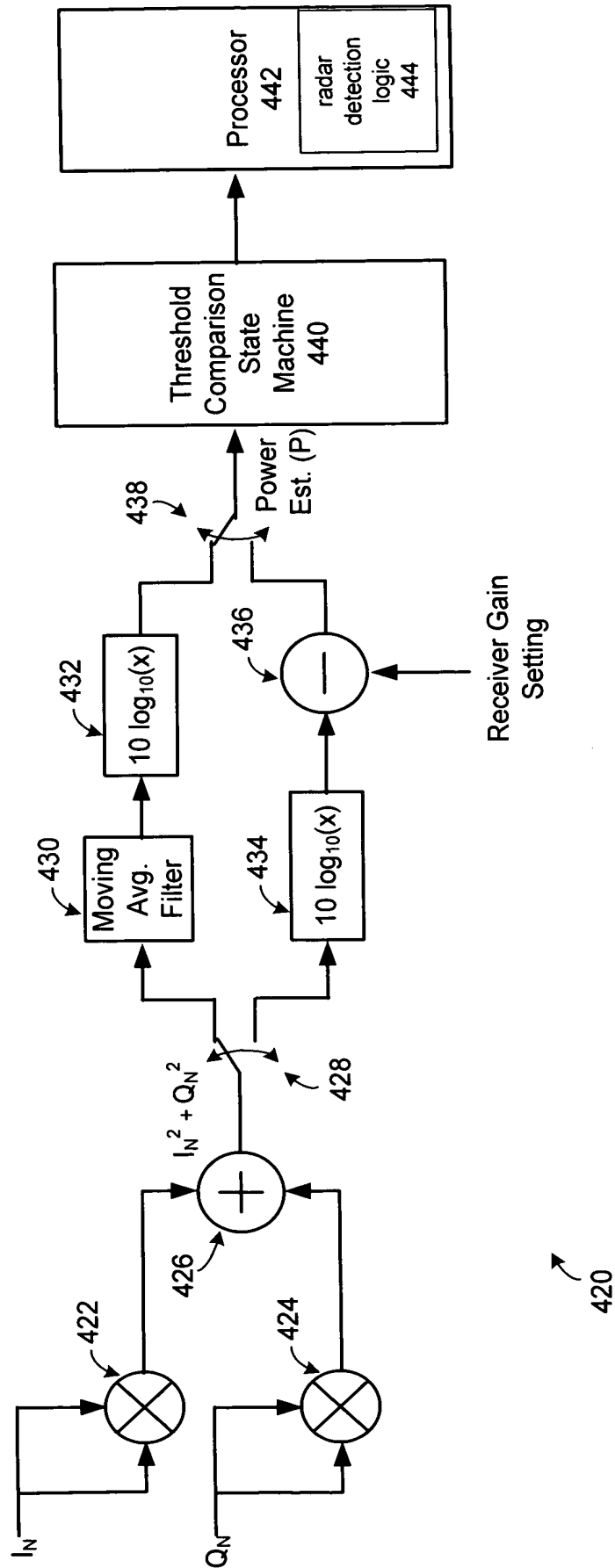
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**FIG. 8** receiver with radar detection block

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To Processor



radar detection logic

FIG. 9



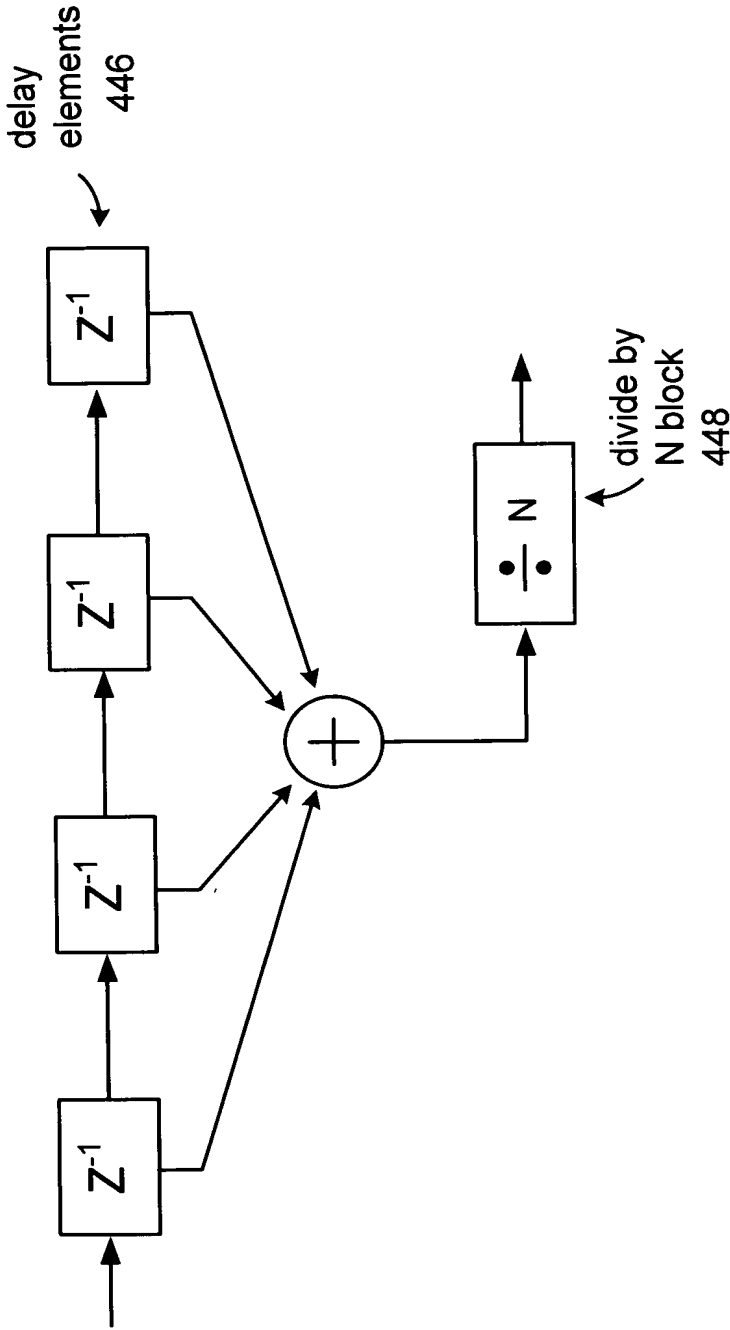
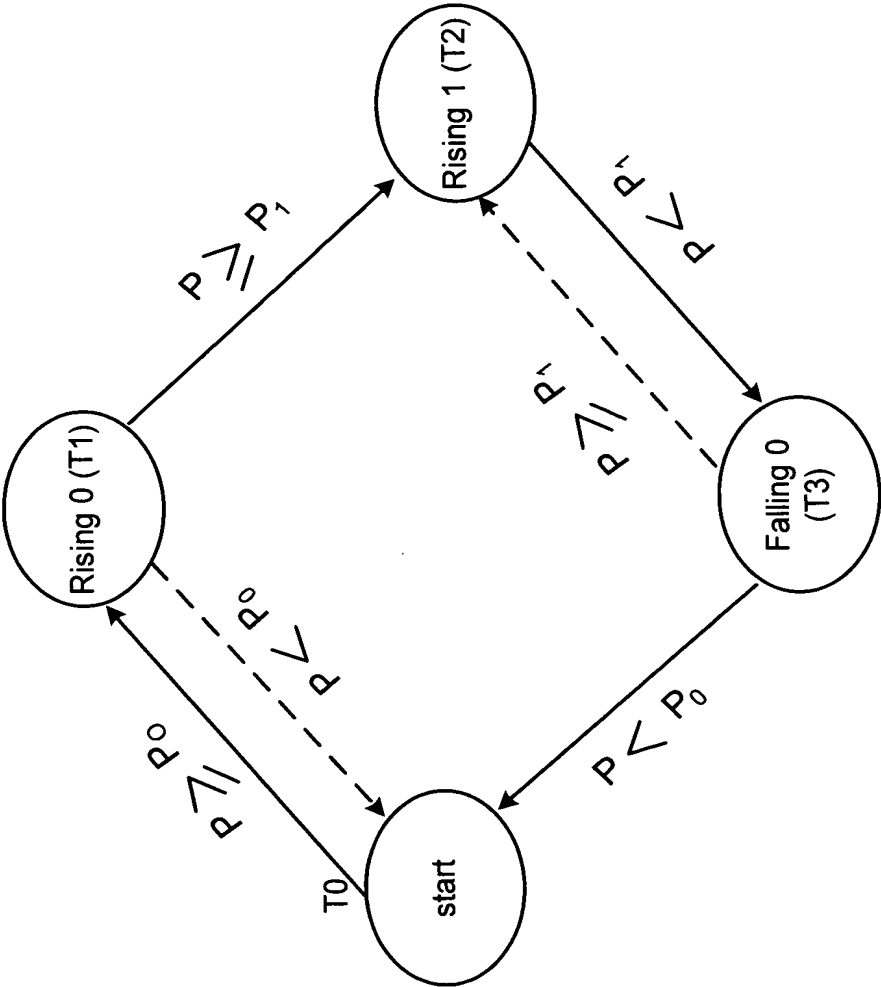
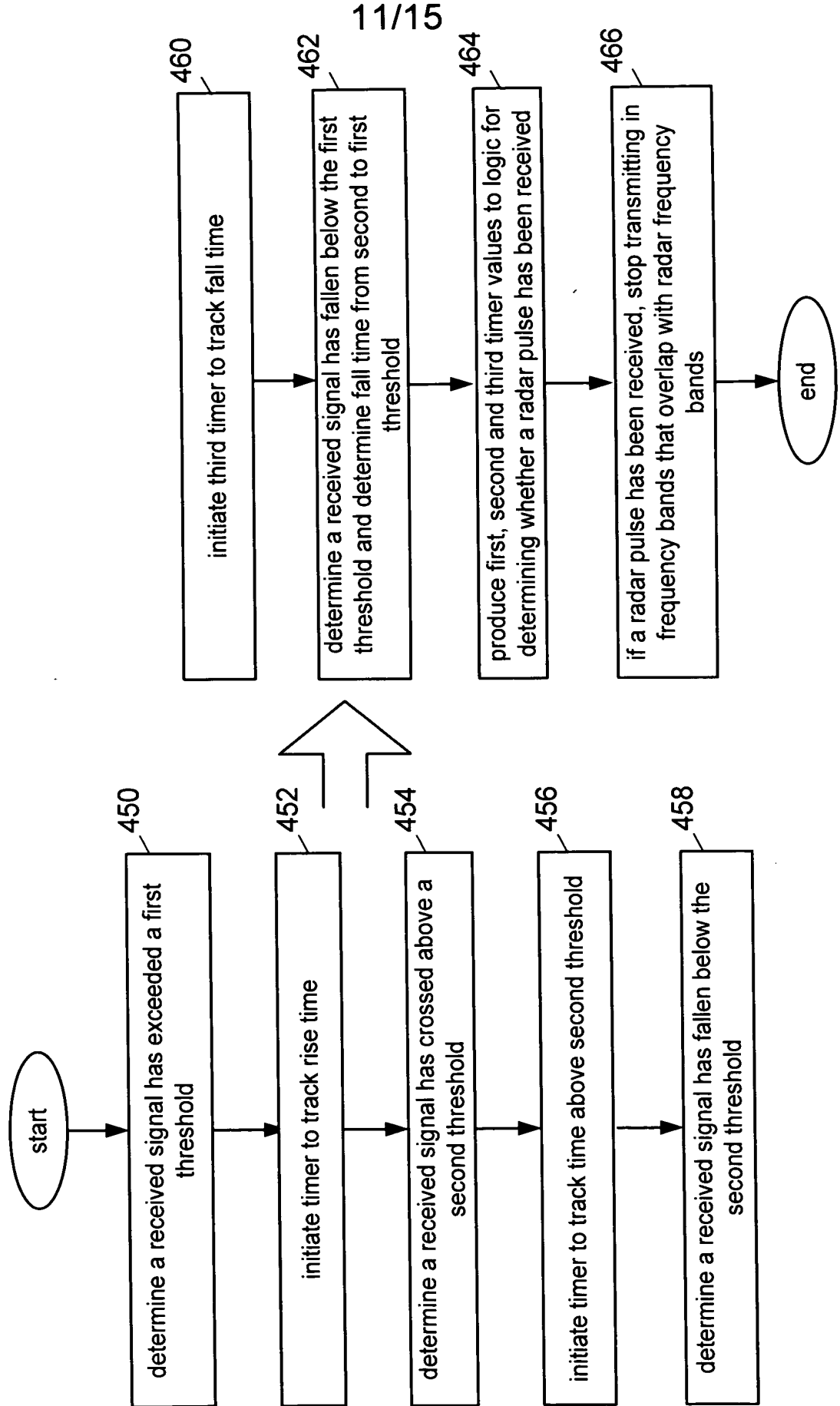


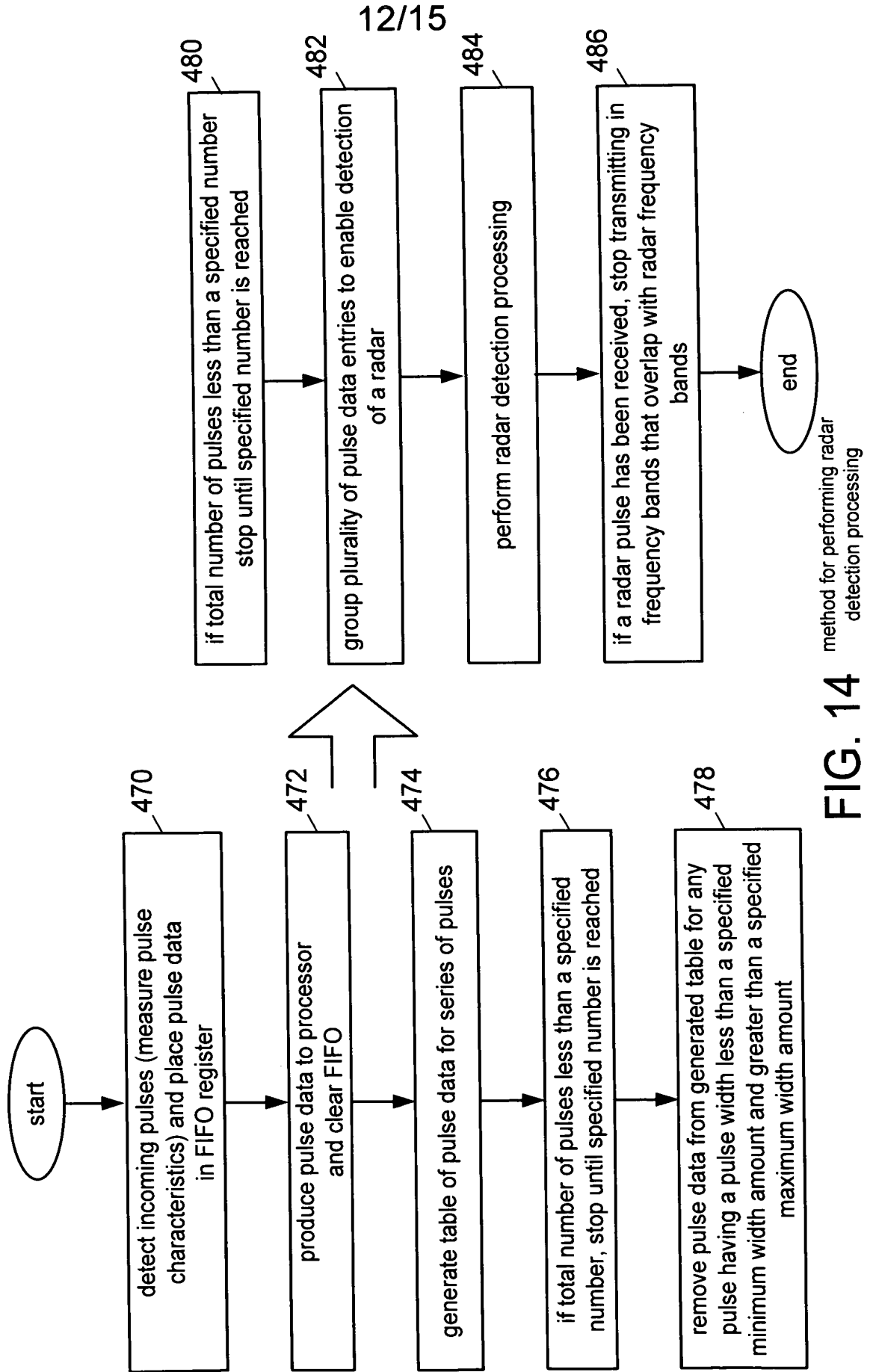
FIG. 11 moving average determination logic



**FIG. 12** state machine for detecting radar pulses



**FIG. 13** method for determining if a radar pulse has been received



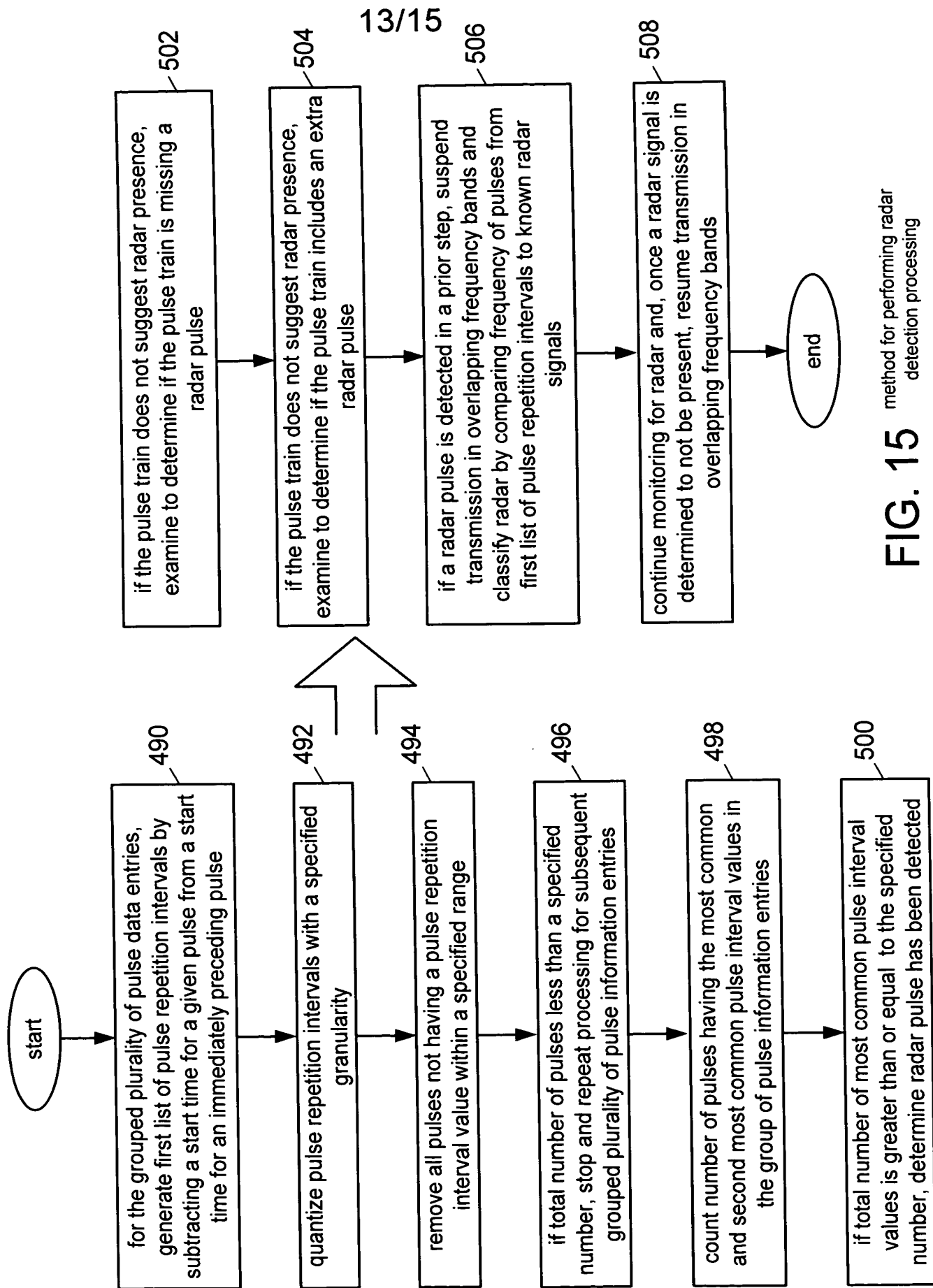


FIG. 15 method for performing radar detection processing

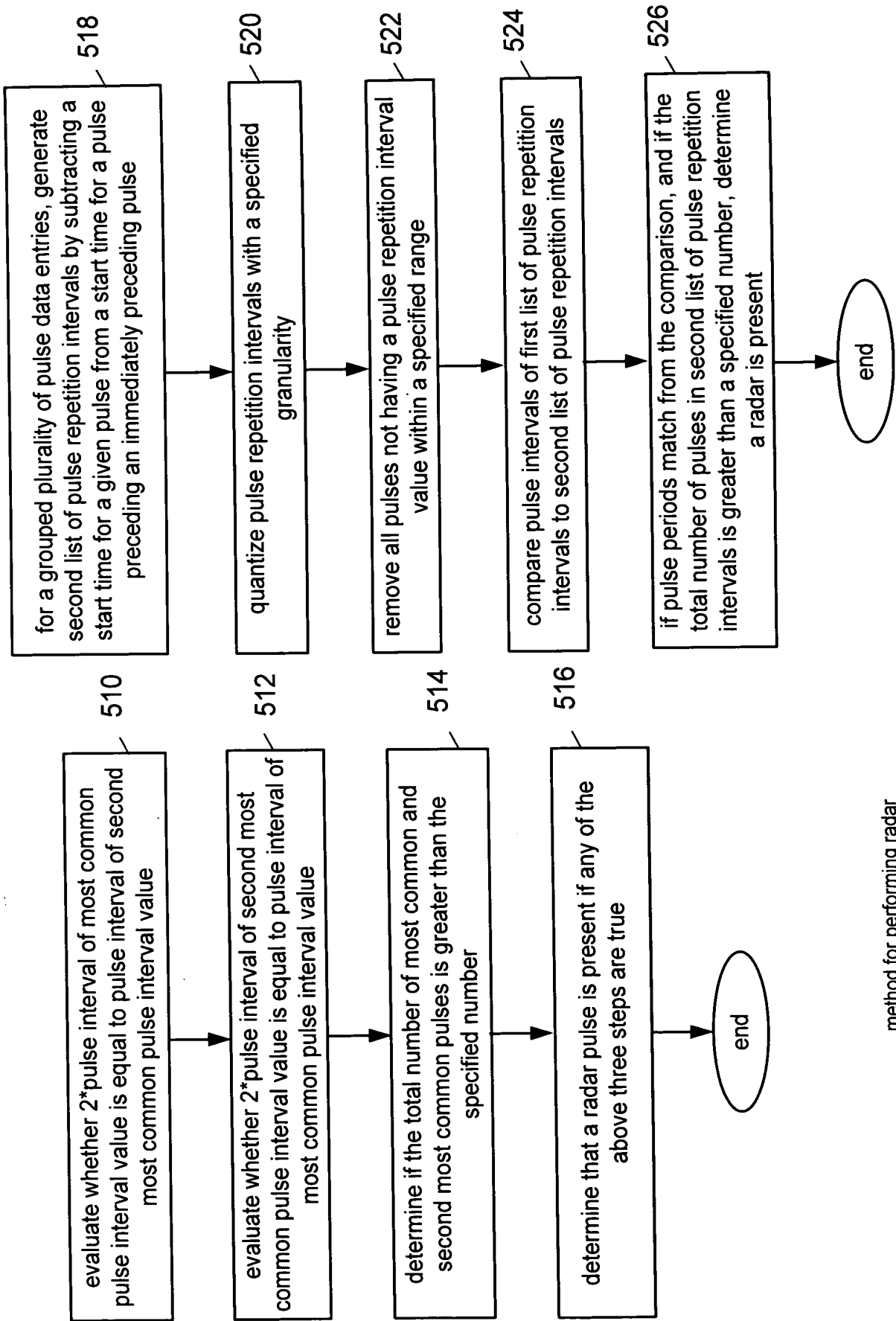


FIG. 16 method for performing radar detection processing for missing pulses

FIG. 17 method for performing radar detection processing for extra pulses

pulse 1

pulse 2

pulse 3

pulse 4

pulse 5

pulse n

start time	rise time	pulse width	fall time
A	B	C	D
E	F	G	H
I	J	K	L
M	N	O	P
Q	R	S	U
W	X	Y	Z

method for performing radar  
detection processing

FIG. 18

pulse interval	pulse interval quantity by type
E - A	n1 I - A
I - E	n2 M - E
M - I	n3 Q - I
Q - M	n4 W - M
W - Q	

pulse interval	pulse interval quantity by type
I - A	n1 I - A
M - E	n2 M - E
Q - I	n3 Q - I
W - M	n4 W - M

method for performing radar  
detection processing for extra pulses

FIG. 20

method for performing radar  
detection processing for  
missing pulses

FIG. 19